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Inventor Name Search Result

Your Search was:

Last Name = ADKISSON
First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11195274	Not Issued	30	08/02/2005	Timestamp generator	ADKISSON, RICHARD
11209215	Not Issued	30	08/23/2005	Start/stop circuit for performance counter	ADKISSON, RICHARD
11523472	Not Issued	25	09/19/2006	Circuitry and method to detect conditions of data	ADKISSON, RICHARD
09887793	Not Issued	161	06/22/2001	System and method for synchronizing data transfer across a clock domain boundary	ADKISSON, RICHARD W.
09887797	Not Issued	161	06/22/2001	SYNC pulse compensation and regeneration in a clock synchronizer controller	ADKISSON, RICHARD W.
10397555	Not Issued	164	03/26/2003	SYSTEM AND METHOD FOR TRANSFERRING DATA FROM A FIRST CLOCK DOMAIN TO A SECOND CLOCK DOMAIN	ADKISSON, RICHARD W.
10420577	6786760	150	04/21/2003	METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10434656	Not Issued	30	05/09/2003	Systems and methods for combining a slow data stream and a fast data stream into a single fast data stream	ADKISSON, RICHARD W.
10434657	6996654	150	05/09/2003	SYSTEMS AND METHODS FOR GENERATING MULTIPLE TRANSACTION IDENTIFIERS TO REDUCED LATENCY IN COMPUTER ARCHITECTURE	ADKISSON, RICHARD W.
10434920	Not Issued	41	05/09/2003	Systems and methods controlling transaction draining for error recovery	ADKISSON, RICHARD W.
10434925	Not Issued	120	05/09/2003	System and method for error capture and logging in computer	ADKISSON, RICHARD W.

				systems	
10435127	7219268	150	05/09/2003	SYSTEM AND METHOD FOR DETERMINING TRANSACTION TIME-OUT	ADKISSON, RICHARD W.
10625291	Not Issued	30	07/23/2003	System and method for effectuating the transfer of data blocks including a header block across a clock boundary	ADKISSON, RICHARD W.
10625365	Not Issued	30	07/23/2003	System and method for effectuating the transfer of data blocks across a clock boundary	ADKISSON, RICHARD W.
10629989	7194650	150	07/30/2003	SYSTEM AND METHOD FOR SYNCHRONIZING MULTIPLE SYNCHRONIZER CONTROLLERS	ADKISSON, RICHARD W.
10630159	7219251	150	07/30/2003	PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10630182	7100065	150	07/30/2003	A CONTROLLER ARRANGEMENT FOR SYNCHRONIZER DATA TRANSFER BETWEEN A CORE CLOCK DOMAIN AND BUS CLOCK DOMAIN EACH HAVING ITS OWN INDIVIDUAL SYNCHRONIZING CONTROLLER	ADKISSON, RICHARD W.
10630297	7239681	150	07/30/2003	SYSTEM AND METHOD FOR MAINTAINING A STABLE SYNCHRONIZATION STATE IN A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10630298	6864722	150	07/30/2003	PHASE DETECTOR FOR A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10630317	7245684	150	07/30/2003	SYSTEM AND METHOD FOR COMPENSATING FOR SKEW BETWEEN A FIRST CLOCK SIGNAL AND A SECOND CLOCK SIGNAL	ADKISSON, RICHARD W.
10635079	Not Issued	25	08/06/2003	Zeroing circuit for performance counter	ADKISSON, RICHARD W.
10635083	Not Issued	41	08/06/2003	General purpose performance counter	ADKISSON, RICHARD W.
10635103	Not Issued	25	08/06/2003	Data selection circuit for performance counter	ADKISSON, RICHARD W.

10635369	Not Issued	25	08/06/2003	Increment/decrement circuit for performance counter	ADKISSON, RICHARD W.
10635371	Not Issued	41	08/06/2003	Coverage circuit for performance counter	ADKISSON, RICHARD W.
10635372	Not Issued	93	08/06/2003	COVERAGE DECODER CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635373	Not Issued	71	08/06/2003	Match circuit for performance counter	ADKISSON, RICHARD W.
10756438	Not Issued	30	01/12/2004	Data Forwarding	ADKISSON, RICHARD W.
10756448	Not Issued	71	01/12/2004	Controlling data delivery	ADKISSON, RICHARD W.
10888897	Not Issued	164	07/08/2004	METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10888898	Not Issued	164	07/08/2004	METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10890782	Not Issued	161	07/14/2004	System and method for observing the behavior of an integrated circuit (IC)	ADKISSON, RICHARD W.
10897729	Not Issued	61	07/23/2004	Drift-tolerant SYNC pulse circuit in a sync pulse generator	ADKISSON, RICHARD W.
10898404	Not Issued	30	07/23/2004	Programmable sync pulse generator	ADKISSON, RICHARD W.
10898693	7119582	150	07/23/2004	PHASE DETECTION IN A SYNC PULSE GENERATOR	ADKISSON, RICHARD W.
10901762	Not Issued	30	07/29/2004	Clock synchronizer	ADKISSON, RICHARD W.
10901773	Not Issued	30	07/29/2004	Controller for clock synchronizer	ADKISSON, RICHARD W.
10902341	Not Issued	30	07/29/2004	Communication among partitioned devices	ADKISSON, RICHARD W.
10944524	Not Issued	30	09/17/2004	Timeout acceleration for globally shared memory transaction tracking table	ADKISSON, RICHARD W.
10945056	Not Issued	30	09/20/2004	Decoded match circuit for performance counter	ADKISSON, RICHARD W.
11021259	Not Issued	25	12/23/2004	Duration minimum and maximum circuit for performance counter	ADKISSON, RICHARD W.
11022021	Not Issued	41	12/23/2004	Edge detect circuit for performance counter	ADKISSON, RICHARD W.
11022023	Not	41	12/23/2004	Match circuit for performing	ADKISSON,

	Issued			pattern recognition in a performance counter	RICHARD W.
11022079	Not Issued	25	12/23/2004	Performance monitoring system	ADKISSON, RICHARD W.
11034152	7002376	150	01/12/2005	PHASE DETECTOR FOR A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
11741560	Not Issued	19	04/27/2007	EVENT DURATION AND SIGNAL VALUE MINIMUM AND MAXIMUM CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
60469120	Not Issued	159	05/09/2003	Programmable clock synchronizer and controller arrangement therefor	ADKISSON, RICHARD W.
60469180	Not Issued	159	05/09/2003	General purpose counters for performance, debug and coverage	ADKISSON, RICHARD W.
60469503	Not Issued	159	05/10/2003	System and method for effectuating the transfer of data blocks across a clock boundary	ADKISSON, RICHARD W.
60469504	Not Issued	159	05/10/2003	System and method for effectuating the transfer of data blocks including a header block across a clock boundary	ADKISSON, RICHARD W.

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Your Search was:

Last Name = ADKISSON
First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60576554	Not Issued	159	06/03/2004	Edge detect circuit for performance counter	ADKISSON, RICHARD W.
60576646	Not Issued	159	06/03/2004	Duratin minimum and maximum circuit for performance counter	ADKISSON, RICHARD W.
60576763	Not Issued	159	06/03/2004	Match circuit for performing pattern recognition in a performance counter	ADKISSON, RICHARD W.
60576764	Not Issued	159	06/03/2004	Performance monitoring system	ADKISSON, RICHARD W.
06607984	4523203	150	05/07/1984	GREY SCALE THERMAL PRINTER CONTROL SYSTEM	ADKISSON, RICHARD W.
08258761	5590304	150	06/13/1994	CIRCUITS, SYSTEMS AND METHODS FOR PREVENTING QUEUE OVERFLOW IN DATA PROCESSING SYSTEMS	ADKISSON, RICHARD W.
09351988	6385676	150	07/12/1999	COHERENT ORDERING QUEUE FOR COMPUTER SYSTEM	ADKISSON, RICHARD W.

Inventor Search Completed: No Records to Display.

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Your Search was:

Last Name = CHONG
First Name = HUAI-TER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10434920	Not Issued	41	05/09/2003	Systems and methods controlling transaction draining for error recovery	CHONG, HUAI-TER V.
10434925	Not Issued	120	05/09/2003	System and method for error capture and logging in computer systems	CHONG, HUAI-TER V.
10435127	7219268	150	05/09/2003	SYSTEM AND METHOD FOR DETERMINING TRANSACTION TIME-OUT	CHONG, HUAI-TER V.
60469503	Not Issued	159	05/10/2003	System and method for effectuating the transfer of data blocks across a clock boundary	CHONG, HUAI-TER V.
60469504	Not Issued	159	05/10/2003	System and method for effectuating the transfer of data blocks including a header block across a clock boundary	CHONG, HUAI-TER V.
10434637	Not Issued	80	05/09/2003	Systems and methods for deleting transactions from multiple fast data streams	CHONG, HUAI-TER VICTOR
10434656	Not Issued	30	05/09/2003	Systems and methods for combining a slow data stream and a fast data stream into a single fast data stream	CHONG, HUAI-TER VICTOR
10435639	Not Issued	71	05/09/2003	Systems and methods to insert broadcast transactions into a fast data stream of transactions	CHONG, HUAI-TER VICTOR
10625365	Not Issued	30	07/23/2003	System and method for effectuating the transfer of data blocks across a clock boundary	CHONG, HUAI-TER VICTOR
10756434	7099977	150	01/12/2004	PROCESSOR INTERRUPT FILTERING	CHONG, HUAI-TER VICTOR
10756438	Not Issued	30	01/12/2004	Data Forwarding	CHONG, HUAI-TER VICTOR

10756448	Not Issued	71	01/12/2004	Controlling data delivery	CHONG, HUAI-TER VICTOR
10902341	Not Issued	30	07/29/2004	Communication among partitioned devices	CHONG, HUAI-TER VICTOR
10944524	Not Issued	30	09/17/2004	Timeout acceleration for globally shared memory transaction tracking table	CHONG, HUAI-TER VICTOR
10625291	Not Issued	30	07/23/2003	System and method for effectuating the transfer of data blocks including a header block across a clock boundary	CHONG, HUAI-TER VICTOR

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